



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Christian Dupuy et al. PATENT APPLICATION
Serial No.: 10/671,200 Group Art Unit: 2816
Filed: September 25, 2003 Examiner:
For: LOW VOLTAGE CIRCUIT FOR INTERFACING
WITH HIGH VOLTAGE ANALOG SIGNALS

Supplemental Information Disclosure Statement

Hon. Commissioner for Patents
Alexandria, VA 22313

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure under 37 CFR § 1.56. A copy of each reference is enclosed:

Other References

Data sheet: Intersil, "Buck and Synchronous-Rectifier (PWM) Controller and Output Voltage Monitor", HIP6004E, October 2001, File No. 4997.1, pp. 1-12.

Data sheet no. PD94251: International Rectifier, "Dual Synchronous PWM Controller with Current Sharing Circuitry and LDO Controller", IRU3046, Sept. 12, 2002, Rev. 1.8, pp. 1-20.

Data sheet: Linear Technology, "3-Phase, 5-Bit VID, 600kHz, Synchronous Buck Switching Regulator Controller, FES LTC3732, May 2002, pp. 1-28.

Data sheet: STMicroelectronics, "5-Bit Programmable Dual-Phase Controller", L6917B, Sept. 2002, pp. 1-33.

Data sheet: ON Semiconductor, "Two-Phase Buck Controller with Integrated Gate Drivers for VRM 9.0", PON CS5332, May 2001, Rev. 10, pp. 1-20.

Data sheet: Maxim Integrated Products, "VRM 9.0, Dual-Phase, Parallelable, Average Current-Mode Controller", MAX5037, July 2002, Rev. 0, pp. 1-30.

Website printout: Maxim Integrated Products, "DC-DC Converter Tutorial", Oct. 19, 2000, pp. 1-9.

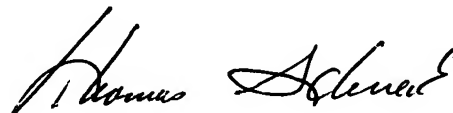
CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Asst. Commissioner for Patents, Alexandria, VA 22313

Signed: 
Typed Name: Sally Azevedo

Date: December 30, 2003

Respectfully submitted,



Thomas Schneck

Reg. No. 24,518

P.O. Box 2-E

San Jose, CA 95109-0005

(408) 297-9733



FORM PTO-1449		Atty. Docket No. ATM-240		Serial No. 10/671,200		
LIST OF PRIOR ART CITED BY APPLICANT		Applicant: Christian Dupuy et al.				
		Filing Date: Sept. 25, 2003		Group: 2816		
U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Grant Date	Name	Class	Sub Class	Filing Date
AA						
AB						
AC						
AD						
AE						
AF						
AG						
FOREIGN PATENT DOCUMENTS						
Examiner Initial*	Document Number	Grant Date	Country	Class	Sub Class	Translation Yes No
AH						
AI						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
AJ	Data sheet: Intersil, "Buck and Synchronous-Rectifier (PWM) Controller and Output Voltage Monitor", HIP6004E, October 2001, File No. 4997.1, pp. 1-12.					
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EXAMINER:				DATE CONSIDERED:		
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						



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Examiner Initial*		Document Number	Grant Date	Name	Class	Sub Class	Filing Date
	BA						
	BB						
	BC						
	BD						
	BE						
	BF						
	BG						
FOREIGN PATENT DOCUMENTS							
Examiner Initial*		Document Number	Grant Date	Country	Class	Sub Class	Translation Yes No
	BH						
	BI						
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